

TM12N02S

N-Channel Enhancement Mosfet

General Description

- Low $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

Applications

- Load switch
- PWM

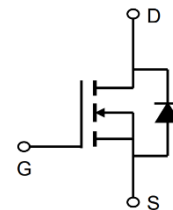
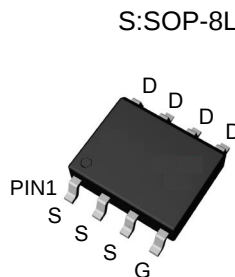
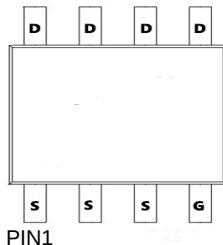
General Features

$V_{DS} = 20V$ $I_D = 12A$

$R_{DS(ON)} = 8m\Omega$ (typ.) @ $V_{GS} = 4.5V$

100% UIS Tested

100% R_g Tested



Marking: 12N02 OR 1012

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	12	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	7.8	A
I_{DM}	Pulsed Drain Current ²	44	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation ³	3	W
$P_D @ T_A = 70^\circ C$	Total Power Dissipation ³	0.86	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	100	$^\circ C/W$

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Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.5	0.75	1.2	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =4.5V, I _D =15A	-	8	11	mΩ
		V _{GS} =2.5V, I _D =10A	-	11	13	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1.0MHz	-	868	-	pF
C _{oss}	Output Capacitance		-	182	-	pF
C _{rss}	Reverse Transfer Capacitance		-	164	-	pF
Q _g	Total Gate Charge	V _{DS} =10V, I _D =15A, V _{GS} =4.5V	-	15	-	nC
Q _{gs}	Gate-Source Charge		-	2	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	5.2	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =10V, I _D =15A, R _{GEN} =3Ω, V _{GS} =4.5V	-	9	-	ns
t _r	Turn-on Rise Time		-	25	-	ns
t _{d(off)}	Turn-off Delay Time		-	37	-	ns
t _f	Turn-off Fall Time		-	14	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	12	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	44	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =30A	-	-	1.2	V

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: $T_J=25^{\circ}\text{C}$, $V_{DD}=10V$, $V_G=4.5V$, $L=0.5mH$, $R_G=25\Omega$, $I_{AS}=9.6A$

3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$



Typical Performance Characteristics

Figure 1: Output Characteristics

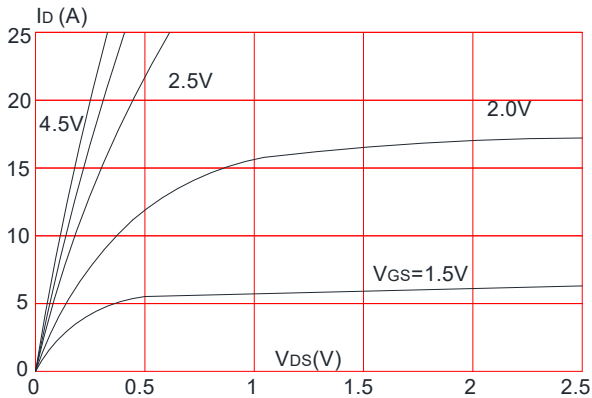


Figure 2: Typical Transfer Characteristics

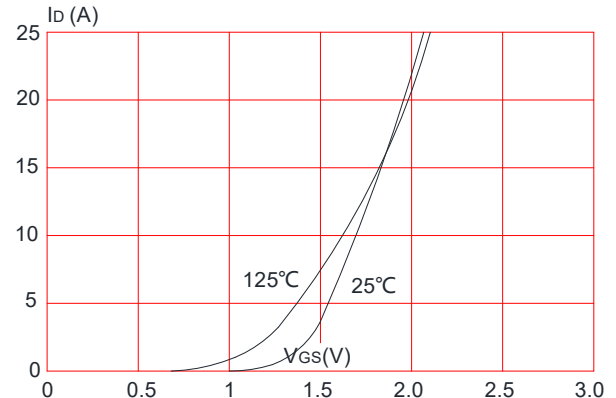


Figure 3: On-resistance vs. Drain Current

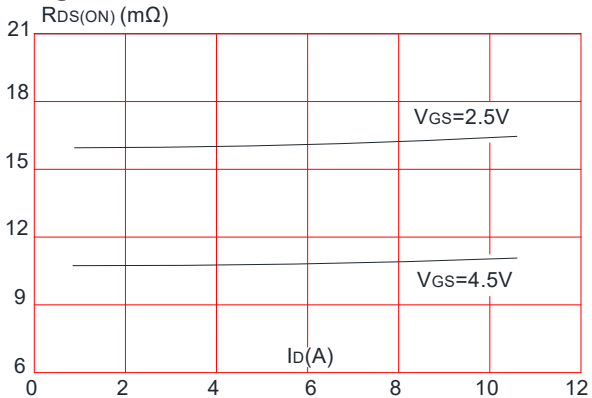


Figure 4: Body Diode Characteristics

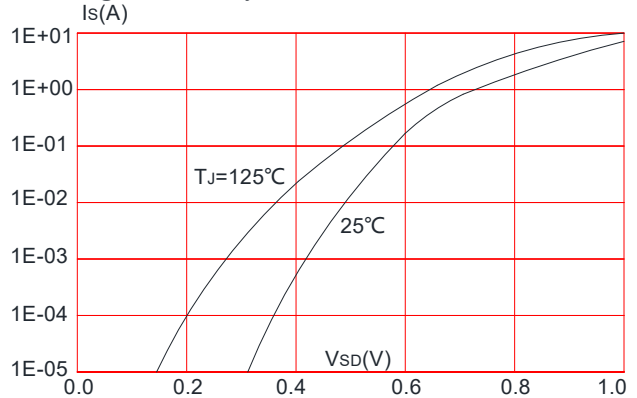


Figure 5: Gate Charge Characteristics

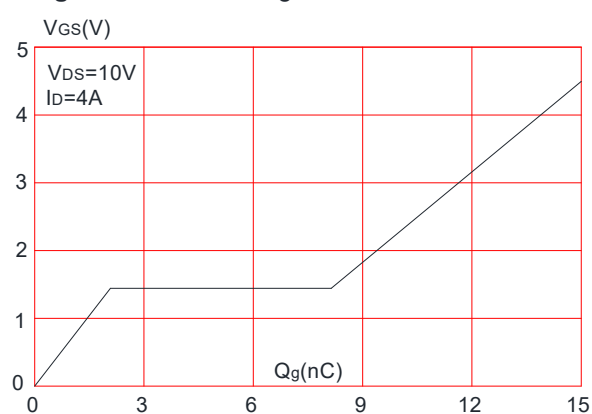
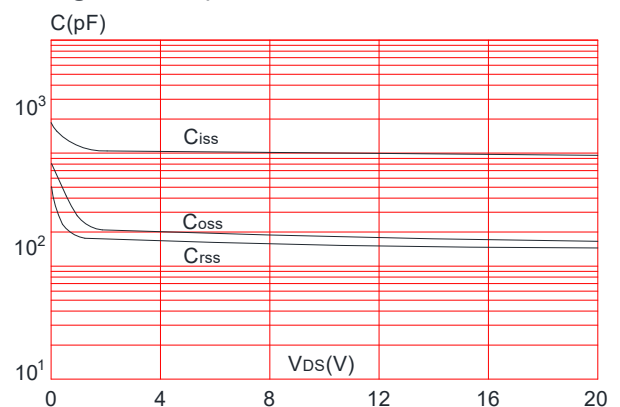


Figure 6: Capacitance Characteristics



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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

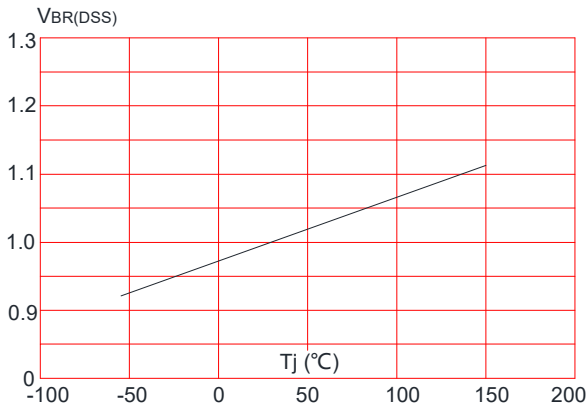


Figure 8: Normalized on Resistance vs. Junction Temperature

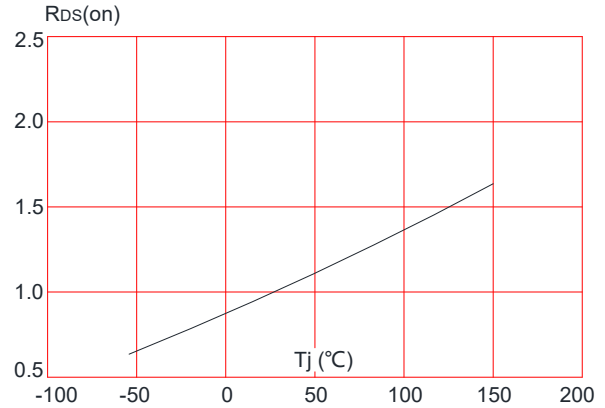


Figure 9: Maximum Safe Operating Area

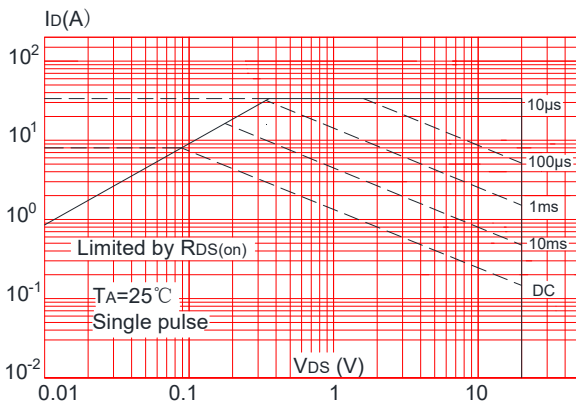


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

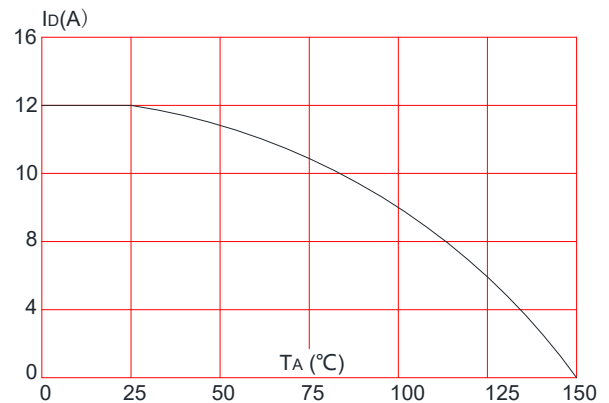
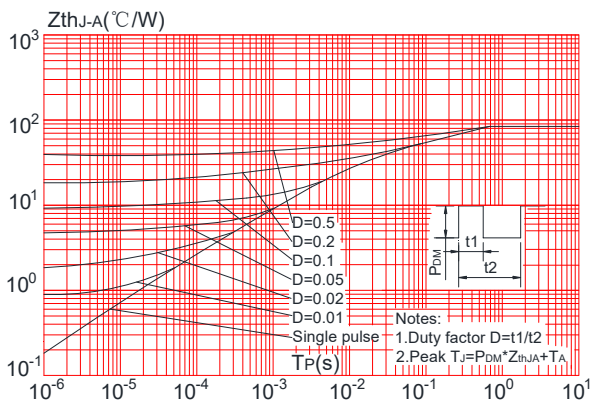
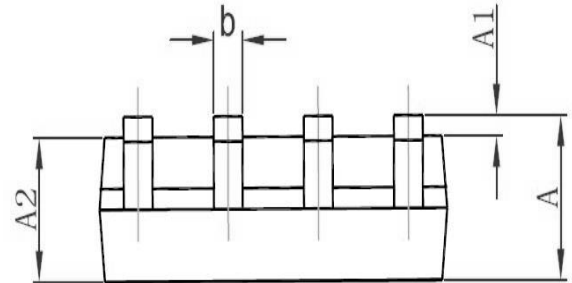
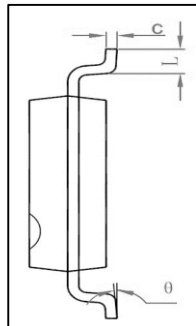
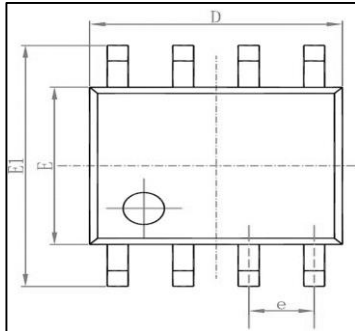


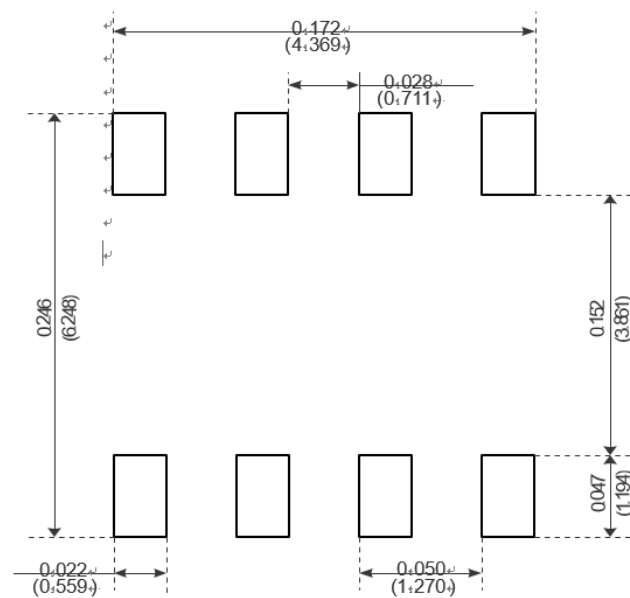
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Package Mechanical Data:SOP-8L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads